

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:)
)
Shunpei YAMAZAKI et al)
)
Serial No: 09/255,605)
)
Filed: February 22, 1999)
)
Art Unit: 2629)
)
Examiner: Vincent E. Kovalick)
)
For: INFORMATION PROCESSING DEVICE)

CORRECTION OF FORM 1449

To: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

On May 10, 2006, Applicant filed an information disclosure statement and PTO form 1449 which contained a typographical error.

Specifically, under "Foreign Patent Documents" on form 1449:

- Item 1 should read: SHINOHARA, T. et al, "High-Performance Polycrystalline TFTs Using Self-Aligned Grain Boundary Control Technique," Electronics and Communications in Japan, part II: Electronics, vol. 76, no. 10, pp. 99-106, (October, 1998).
(To correct the misspelling of "Boundary")

Accordingly, we are submitting a new 1449 form with this information. No new material is being added. If any fee is required in connection with this matter, please charge Deposit Account no. 50/1039.

Respectfully submitted,

A handwritten signature in dark ink, appearing to read 'Mark J. Murphy', is written over a horizontal line.

Mark J. Murphy

Registration No: 34,225

Date: September 18, 2006

COOK, ALEX, McFARRON, MANZO,
CUMMINGS & MEHLER, Ltd.
200 West Adams Street
Suite 2850
Chicago, Illinois 60606
(312) 236-8500

Customer No: 000026568

LIST OF PUBLICATIONS CITED BY APPLICANT	<u>Atty. Docket No.</u> SEL 125	<u>Serial No.</u> 09/255,605
	<u>Applicant</u> Shunpei YAMAZAKI et al	
	<u>Filing Date</u> February 22, 1999	<u>Group</u> 2629

US PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	APPLICANT	English Abstract	English Trans.	FILING DATE

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

(Include name of author (in CAPITAL LETTERS), title of article or item (book, magazine, journal, serial, symposium, catalog, etc.) date, pages(s), volume-issue number(s), publisher, city and/or country where published).

	1) SHINOHARA, T. et al, "High-Performance Polycrystalline Silicon TFTs Using Self-Aligned Grain Boundary Control Technique," Electronics and Communications in Japan, Part II: Electronics, vol. 76, no. 10, pp. 99-106, (October, 1993).
--	---

EXAMINER:	DATE CONSIDERED:
-----------	------------------

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP form. Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.